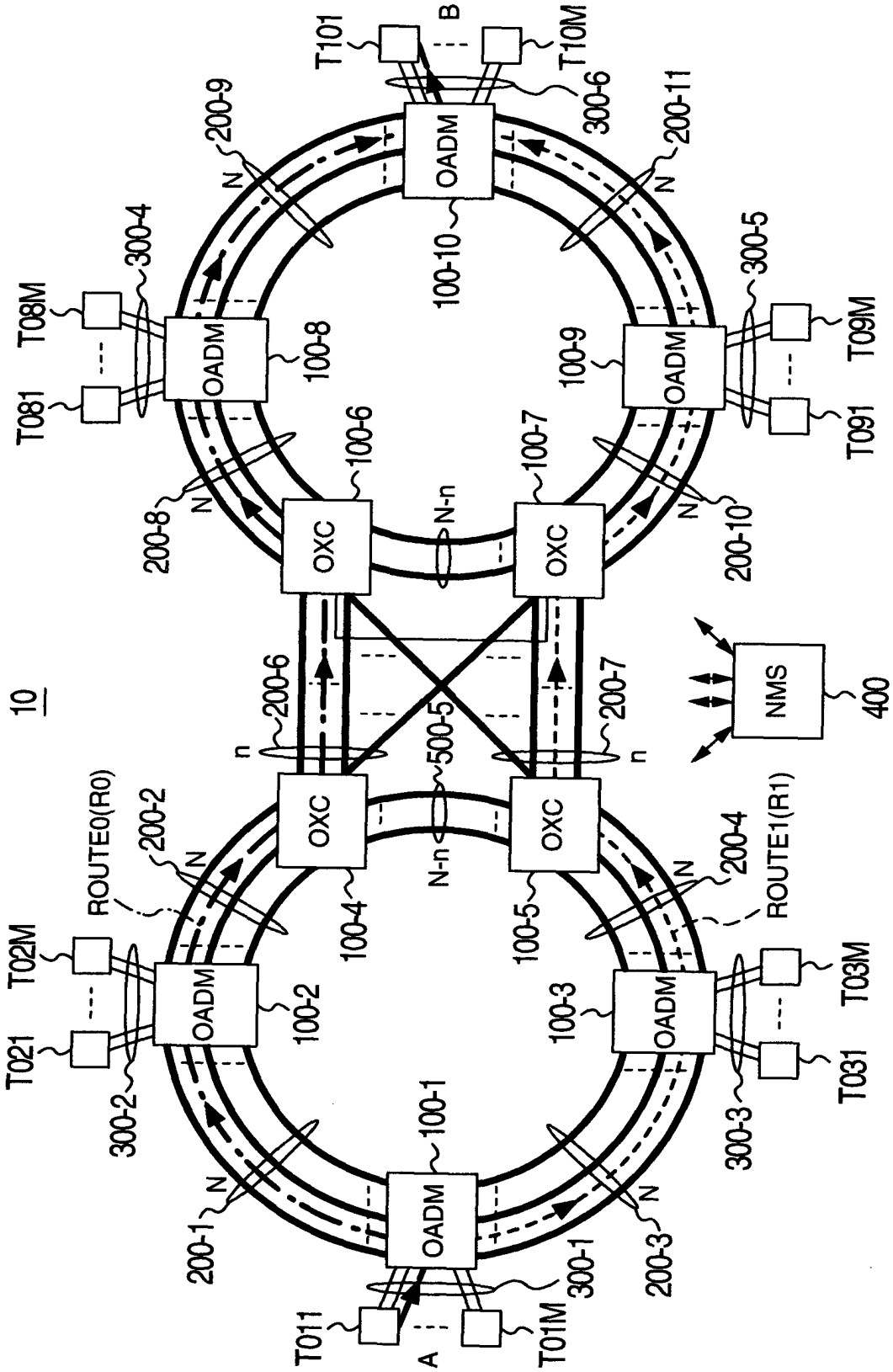
FIG.1



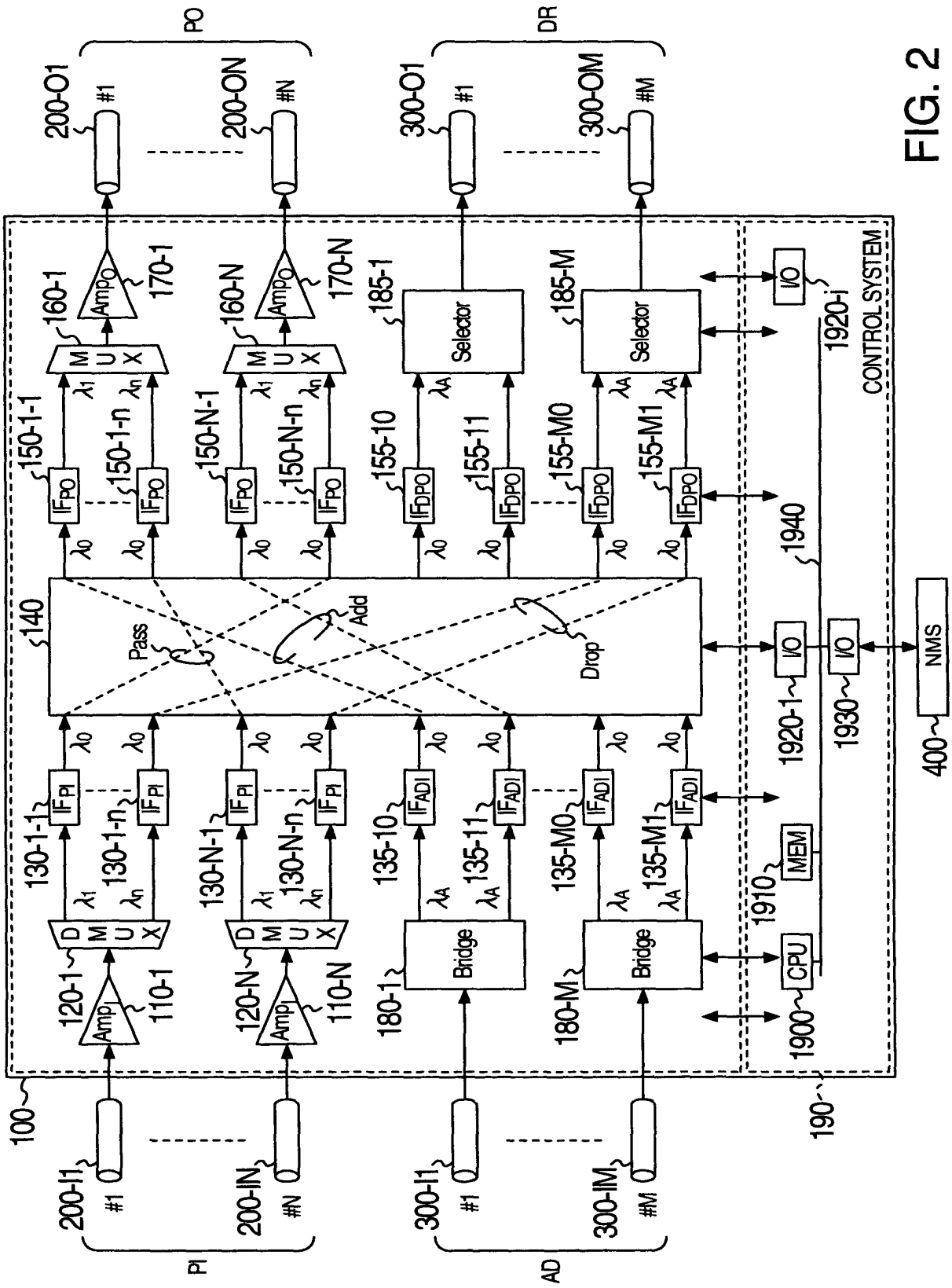

100
190
PI
AD
PO
DR
200-I1
200-IN
300-I1
300-IM
200-O1
200-ON
300-O1
300-OM
#1
#N
#M
120-1
120-N
110-1
110-N
130-1-1
130-1-n
130-N-1
130-N-n
135-10
135-11
135-M0
135-M1
180-1
180-M
Bridge
140
Pass
Add
Drop
150-1-1
150-1-n
150-N-1
150-N-n
155-10
155-11
155-M0
155-M1
160-1
160-N
170-1
170-N
185-1
185-M
Selector
1900
CPU
1910
MEM
1920-1
1920-i
1930
1940
CONTROL SYSTEM
I/O
400
NMS

FIG. 2

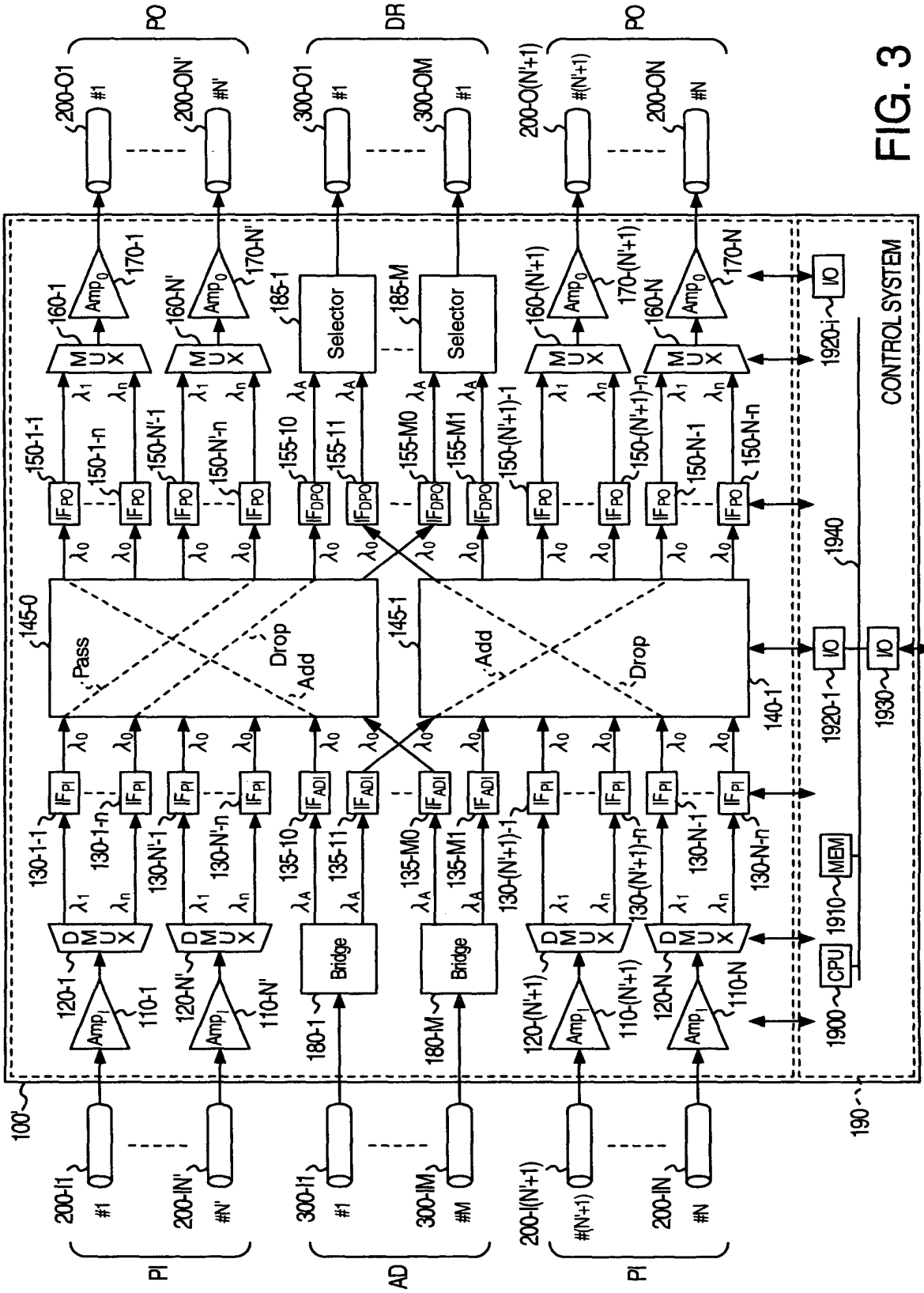

PO
DR
PO
PI
AD
PI
CONTROL SYSTEM
Pass
Drop
Add
Add
Drop
Bridge
Bridge
Selector
Selector
MUX
DMUX
Amp
IF
CPU
MEM
I/O
NMS

FIG. 3

FIG.4

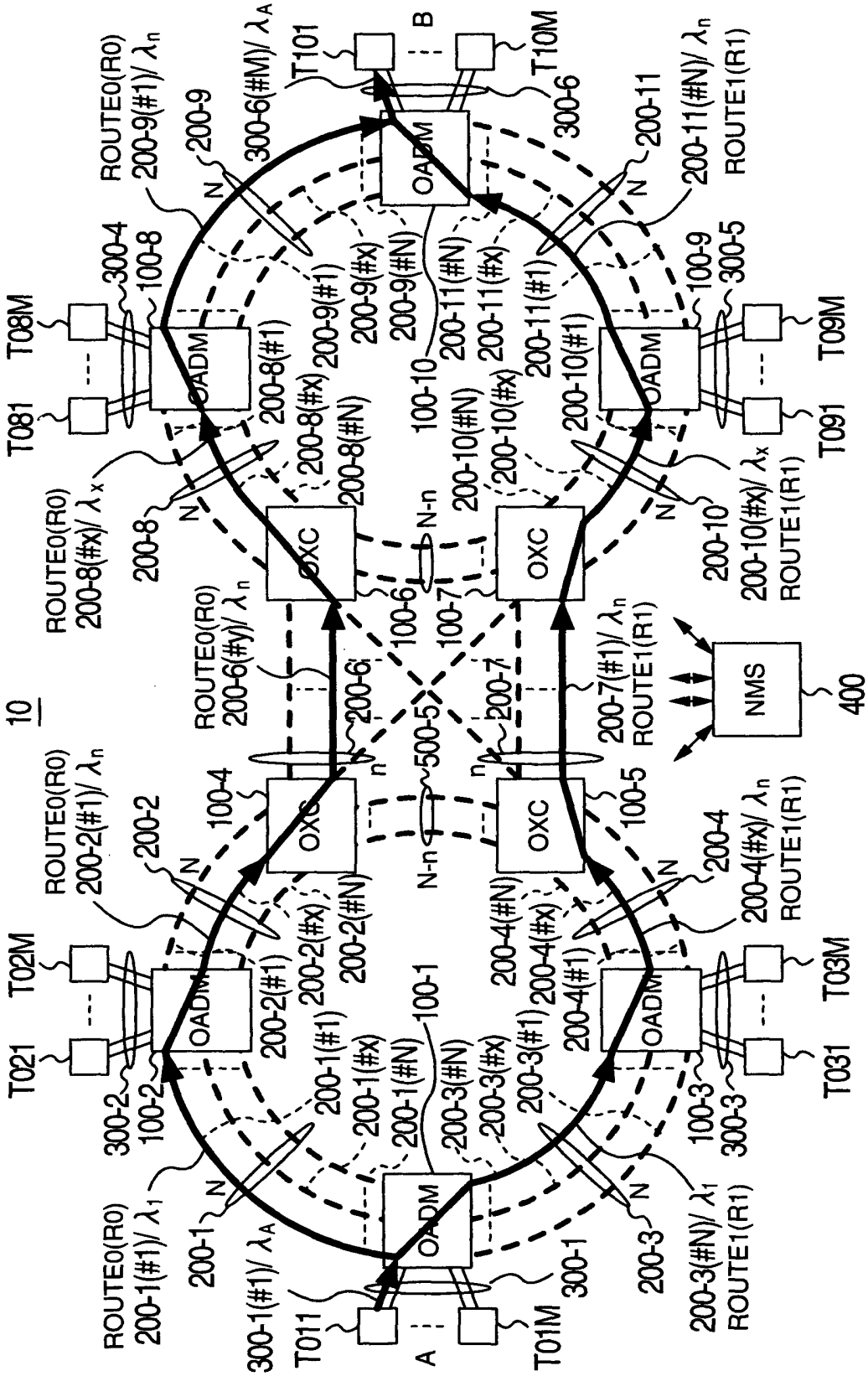

10
OADM
OXC
NMS
400
A
B
T011
T01M
T021
T02M
T031
T03M
T081
T08M
T091
T09M
T101
T10M
ROUTE0(R0)
ROUTE1(R1)
200-1(#1)/ λA
200-1(#x)
200-1(#N)
200-2(#1)
200-2(#x)
200-2(#N)
200-3(#1)
200-3(#x)
200-3(#N)/ λ1
200-4(#1)
200-4(#x)
200-4(#N)
200-6(#y)/ λn
200-7(#1)/ λn
200-8(#1)
200-8(#x)
200-8(#N)
200-8(#x)/ λx
200-9(#1)
200-9(#x)
200-9(#N)
200-9(#1)/ λn
200-10(#1)
200-10(#x)
200-10(#N)
200-10(#x)/ λx
200-11(#1)
200-11(#x)
200-11(#N)/ λn
300-1(#1)/ λA
300-6(#M)/ λA
100-1
100-2
100-3
100-4
100-5
100-6
100-7
100-8
100-9
100-10
200-1
200-2
200-3
200-4
200-6
200-7
200-8
200-9
200-10
200-11
300-1
300-2
300-3
300-4
300-5
300-6
500-5
N
N-n
n

FIG. 5

4000

		OADM (Add)	OADM (Pass)	OXC (Pass)	OXC (Pass)	OADM (Pass)	OADM (Drop)
ROUTE 0 (R0)	EQUIPMENT	100-1	100-2	100-4	100-6	100-8	100-10
	INPUT FIBRE / WAVELENGTH	300-1(#1) / λ_A	200-1(#1) / λ_1	200-2(#1) / λ_n	200-6(#y) / λ_n	200-8(#x) / λ_x	200-9(#1) / λ_n
	OUTPUT FIBRE / WAVELENGTH	200-1(#1) / λ_1	200-2(#1) / λ_n	200-6(#y) / λ_n	200-8(#x) / λ_x	200-9(#1) / λ_n	300-6(#M) / λ_A
ROUTE 1 (R1)	EQUIPMENT	100-1	100-3	100-5	100-7	100-9	100-10
	INPUT FIBRE / WAVELENGTH	300-1(#1) / λ_A	200-3(#N) / λ_1	200-4(#x) / λ_n	200-7(#1) / λ_n	200-10(#x) / λ_x	200-11(#N) / λ_n
	OUTPUT FIBRE / WAVELENGTH	200-3(#N) / λ_1	200-4(#x) / λ_n	200-7(#1) / λ_n	200-10(#x) / λ_x	200-11(#N) / λ_n	300-6(#M) / λ_A